

## Description

# [NON-COHERENT FREQUENCY SHIFT KEYING TRANSMITTER USING A DIGITAL INTERPOLATION SYNTHESIZER]

### CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the priority benefit of Taiwan application serial no. 92124167, filed September 02, 2003.

### BACKGROUND OF INVENTION

[0002] Field of the Invention

[0003] This invention generally relates to a non-coherent frequency shift keying transmitter, and more particularly to a non-coherent frequency shift keying transmitter using a digital interpolation synthesizer.

[0004] Description of Related Art

[0005] Digital wireless communication system, such as mobile phone communication system using bluetooth protocol, has been widely used by the consumers and becomes an indispensable tool for a modern daily life. In a digital

wireless communication system, because of the high efficiency requirement for transmission, the non-coherent frequency shift keying (FSK) technology plays an important role in generating RF signals for carrying the transmitting signals.

[0006] In a digital wireless communication system, a non-coherent FSK modulator includes a micro processing unit (MPU) and a phase locked loop (PLL). The non-coherent FSK modulator converts the signals from the baseband frequency to radio frequency (RF). FIG. 1 is a block diagram of a conventional non-coherent FSK modulator 100. Referring to FIG. 1, the MPU 102 receives the source signal; the source signal is up-converted by the PLL (104, 106, and 108), processed by the power amplifier, and then transmitted. The PLL includes a phase frequency detector 104, a loop filter 106, and a voltage controlled oscillator (VCO) 108 as shown in FIG. 1.

[0007] However, the conventional non-coherent FSK modulator has the following disadvantages. First, the PLL requires a relatively long period for settling time to transmit a precise frequency and thus it is power consuming. Further, the quality of the signal transmitted by the modulator is neither satisfying. An analog circuit designer has to trade

off between signal stability and short lock time, yet usually neither condition is met. Further, the modulator suffers from fabrication migration thus transmission quality is degraded for limitation of PLL bandwidth.

## **SUMMARY OF INVENTION**

[0008] An object of the present invention is to avoid unnecessary analog circuits and use succinct analog circuits to implement the PLL in the transmitter.

[0009] Another object the present invention is to provide a non-coherent frequency shift keying transmitter using a digital interpolation synthesizer.

[0010] Still another object the present invention is to provide a non-coherent frequency shift keying transmitting system using a digital interpolation synthesizer.

[0011] The present invention provides a non-coherent frequency shift keying transmitter. A micro processing unit receives the baseband signal and generates a digital signal sequence corresponding to the baseband signal. A frequency synthesizer, coupled to the micro processing unit, synthesizes the digital signal sequence to a plurality of synthesized signals such as a 104kHz intermediate frequency signal and a 96kHz intermediate frequency signal. The frequency synthesizer is a digital frequency synthe-

sizer applying the interpolation. The digital frequency synthesizer stores a sequence having two frequencies and applies interpolation to synthesize a frequency between these two frequencies. Further, the frequency synthesizer can also be used as a frequency hopping mechanism with a numerical controlled oscillator to up-convert the signal to a higher frequency such as 2MHz. The cascaded integrator-comb filter then removes the noise. Then the digital signal sequences are converted to analog signals. The local oscillator is used to generate a RF signal. Then a band-pass filter removes the noise. Finally, the modulated RF signal is amplified and transmitted.

[0012] In the present invention, a digital frequency synthesizer is used to replace a portion of analog circuits. Hence, the fabrication migration would be reduced during the fabrication of the circuits. Further, the modulated RF signal is more stable than that of the conventional modulator, has a more efficient spectrum control, and has a better performance for the receiver.

[0013] In addition, the present invention uses digital interpolation synthesizer. Compared to the transmitting signal generated by a conventional analog PLL, the transmitting signal of the present invention is more stable; i.e., within

the same frequency band of the spectrum, the utilization rate of the spectrum is much higher. Further, the performance on the receiver side is much better.

[0014] The above is a brief description of some deficiencies in the prior art and advantages of the present invention. Other features, advantages and embodiments of the invention will be apparent to those skilled in the art from the following description, accompanying drawings and appended claims.

#### **BRIEF DESCRIPTION OF DRAWINGS**

[0015] FIG. 1 is a block diagram illustrating a conventional non-coherent FSK modulator.

[0016] FIG. 2 is a block diagram illustrating a non-coherent FSK modulator in accordance with a preferred embodiment of the present invention.

#### **DETAILED DESCRIPTION**

[0017] FIG. 2 is a block diagram of a non-coherent FSK modulator 200 in accordance with a preferred embodiment of the present invention. The non-coherent FSK modulator 200 includes a micro processing unit 202, a digital synthesizer 204, a numerical controlled oscillator (NCO) 206, a cascaded integrator-comb (CIC) filter 208, a digital-analog

(D/A) converter 210, a local oscillator 212, a band-pass filter 214, a power amplifier 216, and a transmitting end 218. The digital synthesizer 204, the numerical controlled oscillator (NCO) 206, the cascaded integrator-comb (CIC) filter 208, and the digital-analog (D/A) converter 210 are deemed to be an intermediate frequency processor. The local oscillator 212, the band-pass filter 214, the power amplifier 216, and the transmitting end 218 are deemed to be a radio frequency processor. The micro processing unit 202 receives the baseband signal and generates a digital signal sequence corresponding to the baseband signal inputting to the intermediate frequency processor. The digital synthesizer 204 synthesizes the digital signal sequence to two synthesized signals such as a 104kHz intermediate frequency signal and a 96kHz intermediate frequency signal. The numerical controlled oscillator up-converts these two signals to a higher frequency such as 2MHz. The cascaded integrator-comb filter 208 then removes the noise. Then the digital signal sequences are converted to analog signals by the D/A converter 210. Hence, before the signals enter the D/A converter 210, the present invention provides a digital structure to replace the conventional PLL circuit.

[0018] Referring to FIG. 2, the local oscillator 212 up-converts the analog signal to a modulated RF signal. Then a band-pass filter 214 removes the noise. Finally, the modulated RF signal is amplified by the power amplifier 216 and transmitted through the transmitting end 218.

[0019] The present invention also provides a method for transmitting a non-coherent frequency shift keying, for up-converting a baseband signal to a radio frequency signal. The method comprises: receiving said baseband signal and generating a digital signal sequence corresponding to said baseband signal; synthesizing said digital signal sequence to a plurality of synthesized signals; up-converting said plurality of synthesized signals to an intermediate frequency signal with a first oscillating method; removing a noise of said intermediate frequency signal with a first filtering method; converting said noise-removed intermediate frequency signal to an analog signal; up-converting said analog signal to a radio frequency signal with a second oscillating method; and removing a noise of said radio frequency signal with a second filtering method; and amplifying said noise-removed radio frequency signal and transmitting said amplified radio frequency signal.

[0020] The step of synthesizing said digital signal sequence to a plurality of synthesized signals is performed by a digital frequency synthesis method. The first oscillating method is a numerical controlled oscillating method. The first filtering is a cascaded integrator-comb filtering method. The second oscillating method is a local filtering method. The second filtering method is an analog band-pass filtering method.

[0021] The above description provides a full and complete description of the preferred embodiments of the present invention. Various modifications, alternate construction, and equivalent may be made by those skilled in the art without changing the scope or spirit of the invention. Accordingly, the above description and illustrations should not be construed as limiting the scope of the invention which is defined by the following claims.